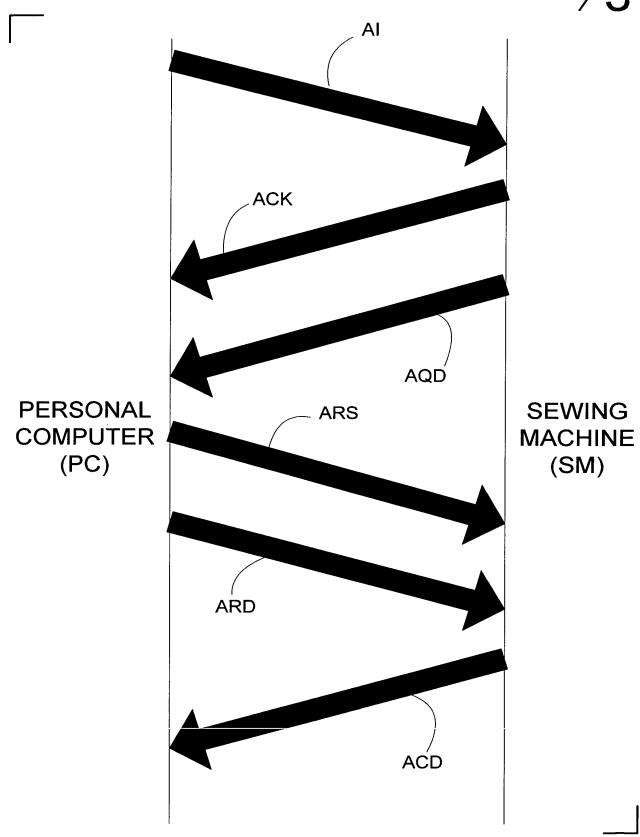
FIG. 3/3



SM COMMUNICATION PORT Microprocessor SRAM FIG. 1/3 AUTHORIZATION PROTOCOL C PC COMMUNICATION PORT CPU RAM